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Technology Center 2100

**ABSTRACT OF THE DISCLOSURE**

A serial bus data control device for communication devices to receive data in a packet format is provided which is capable of obtaining each piece of actual data contained in each of packets needed for reconstructing a series of original data by storing actual data contained in two or more received packets in a memory location with a continued address in a buffer. The serial bus data control device includes a preprocessing section to recognize each of the packets received through the serial bus and to divide the actual data contained in each of the packets into a plurality of unit length data portions having a predetermined length, and a storing section to store the actual data portions contained in each of the packets recognized by the preprocessing section. The preprocessing section has an address control circuit to perform addressing to store each of the unit length data constituting the actual data contained in each of the packets.